

FIG. 1

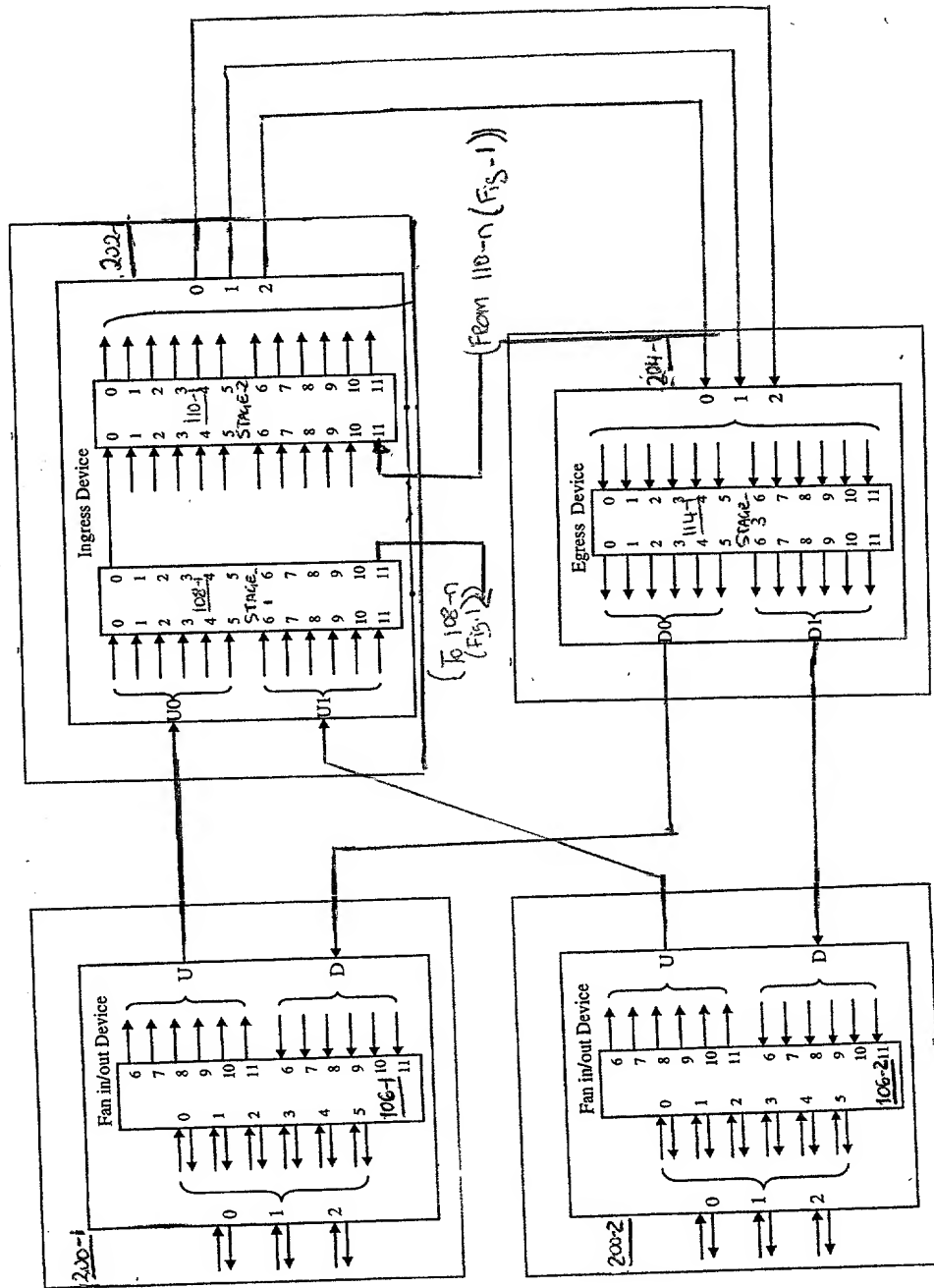


FIG. 2

104-1

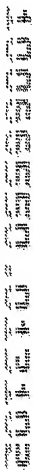
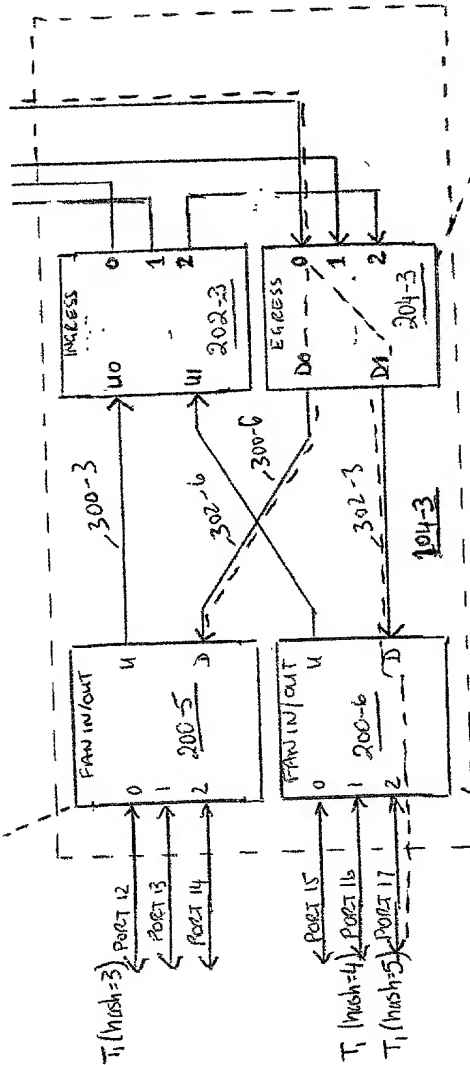


FIG. 3



436 438 440

Matrix	Egress Port	Trunk Table	Port
EST	0 1 2 U	0 1 2 U	
S	0 0 1 1 0	0 0 1 1 1	
A	1 0 1 1 1	1 0 1 1 1	
S	2 0 1 1 1	A 2 0 1 1 1	
P	3 1 1 1 1	S 3 1 1 1 1	
	4 0 1 1 1	H 4 0 1 1 1	
	5 0 1 1 1	S 5 0 1 1 1	



424 426 428

Matrix	Port	Trunk Table	Port
EST	0 1 1	0 1 1	
S	1 1 1	1 1 1	
A	2 1 1	A 2 1 1	
S	3 1 1	S 3 1 1	
H	4 1 1	H 4 1 1	
S	5 1 1	S 5 1 1	

430 432 434

Matrix	Egress Port	Trunk Table	Port
EST	0 1 2 U	0 1 2 U	
S	0 1 0 0 1	0 1 0 0 1	
A	1 1 0 0 1	1 1 0 0 1	
S	2 1 0 0 1	A 2 1 0 0 1	
P	3 1 0 0 1	S 3 1 0 0 1	
	4 1 1 0 1	H 4 1 1 0 1	
	5 1 1 0 1	S 5 1 1 0 1	

FIG. 4B

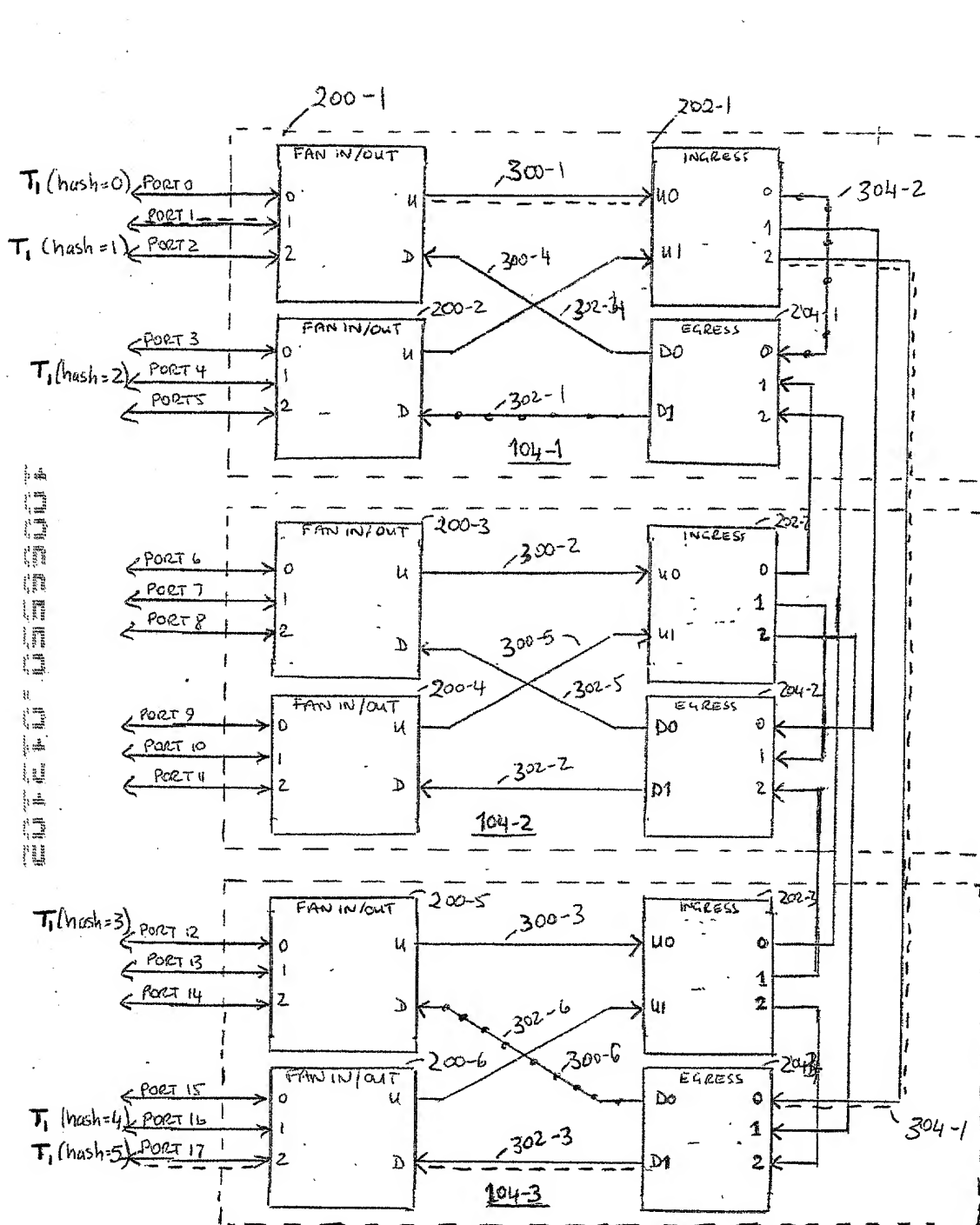
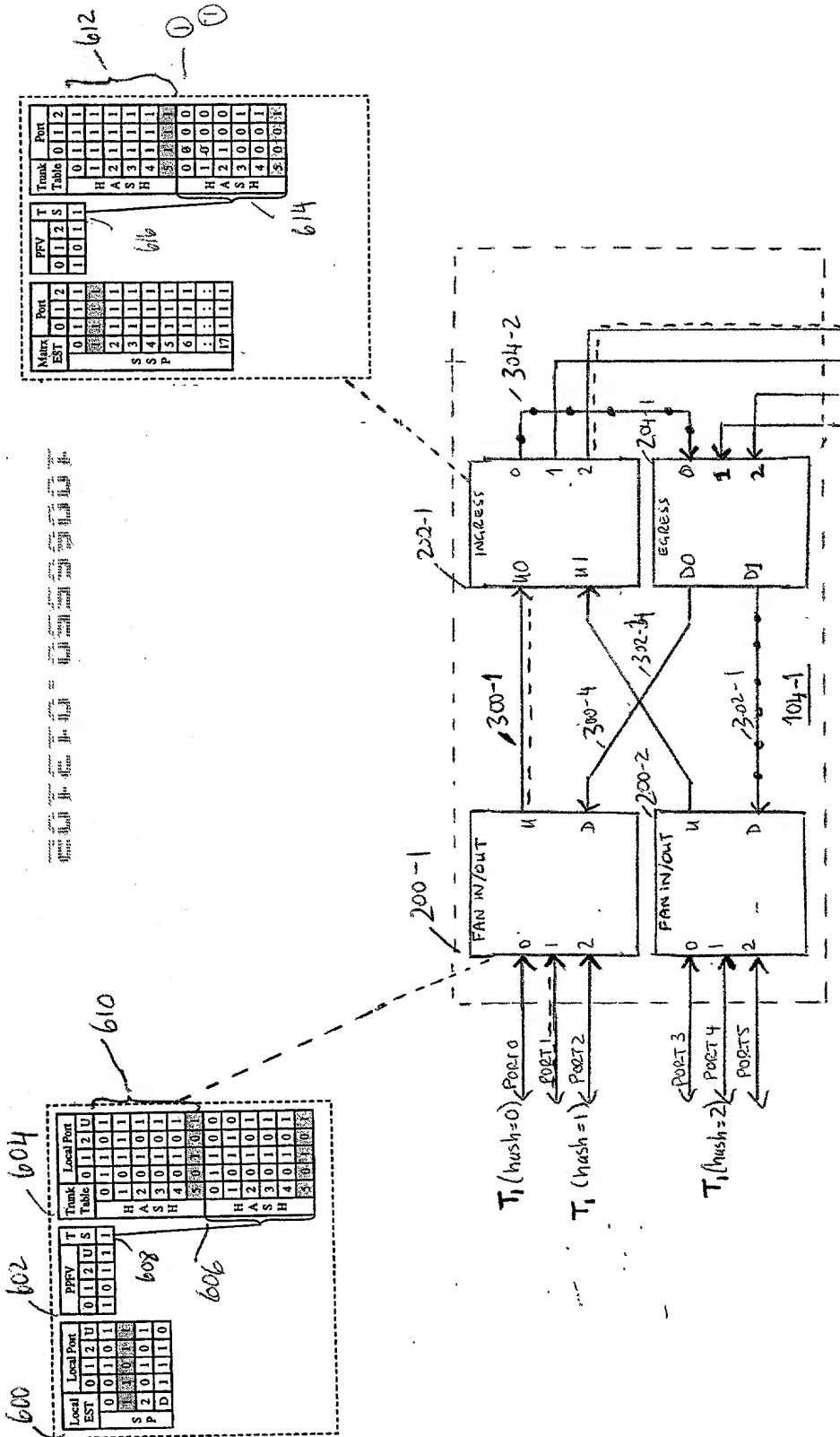


FIG. 5



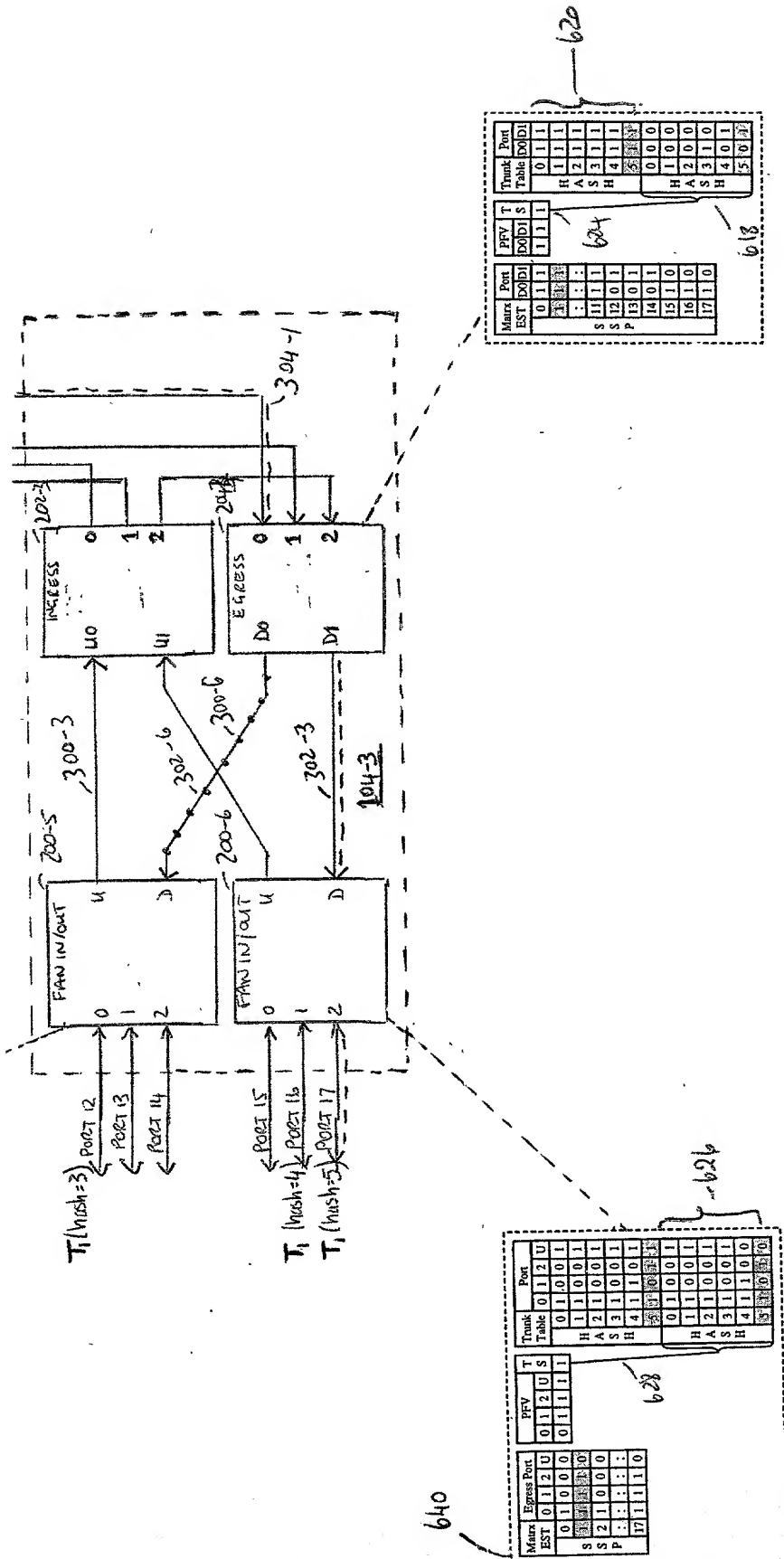
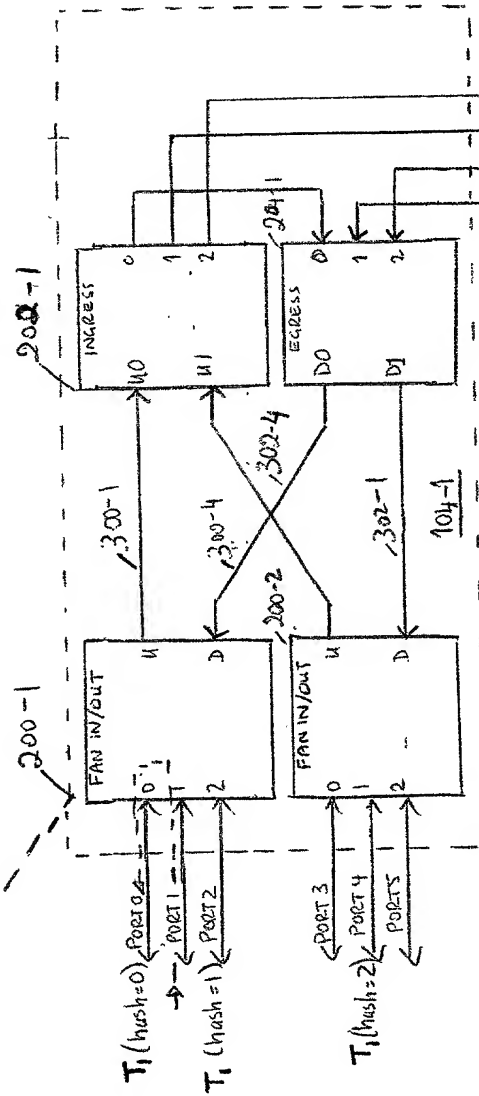


FIG. 6B



761

800

PHYSICAL LAYER (L1) HEADER <u>802</u>	DATA LINK LAYER (L2) HEADER <u>804</u>	NETWORK LAYER (L3) HEADER <u>806</u>	TRANSPORT LAYER (L4) HEADER <u>808</u>	DATA <u>810</u>	CHECKLIST <u>812</u>
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PRIOR ART
FIG. 8 A

804

L2 DESTINATION ADDRESS (DA) (6 BYTES) <u>814</u>	L2 SOURCE ADDRESS (SA) (6 BYTES) <u>816</u>	VLAN ID (OPTIONAL) (12 BITS) TAG PROTOCOL IDENTIFIER (TPID) <u>818a</u>	TAG CONTROL INFORMATION (TCI) <u>818b</u>	LENGTH/ TYPE (2 BYTES) <u>820</u>
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PRIOR ART
FIG. 8 B

806			
<u>822</u>	<u>834</u>	<u>836</u>	<u>828</u>
VERS	HLEN	TOS	TOTAL LENGTH
<u>830</u>			<u>832</u>
IDENTIFICATION			FLAGS
			<u>834</u>
			FRAGMENT OFFSET
<u>836</u>	<u>840</u>		<u>842</u>
TTL	PROTOCOL		HEADER CHECKSUM
<u>844</u>			
IP SOURCE ADDRESS			
<u>846</u>			
IP DESTINATION ADDRESS			
<u>848</u>			<u>850</u>
OPTIONS			PAD

PRIOR ART
FIG. 8 C

202-1

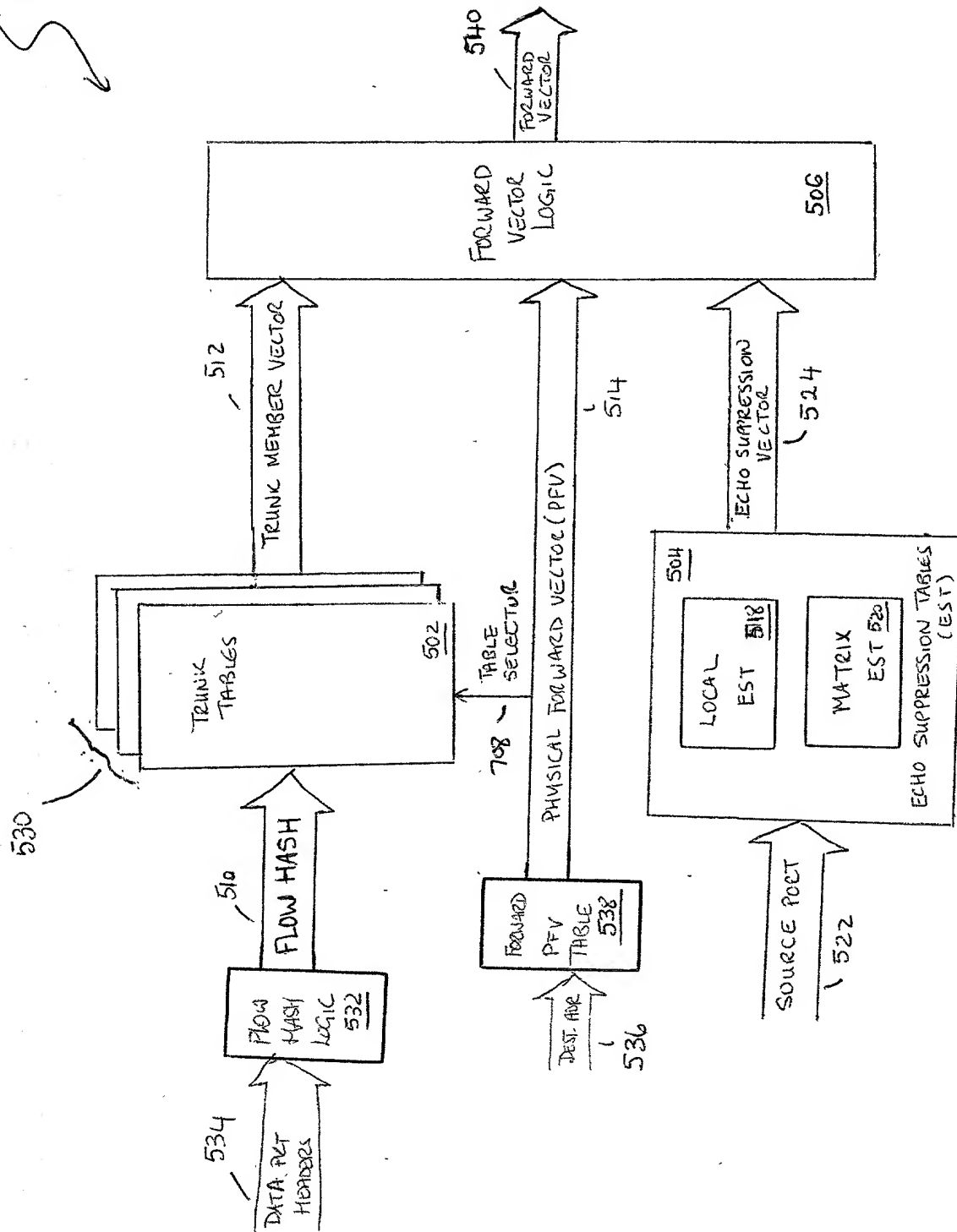


FIG. 9

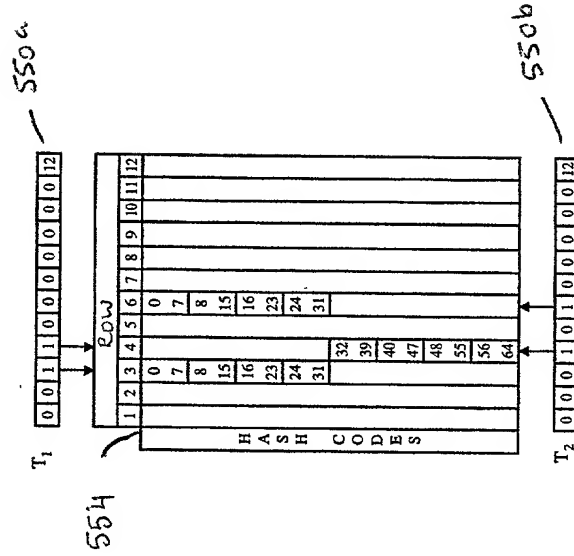


FIG. 10B

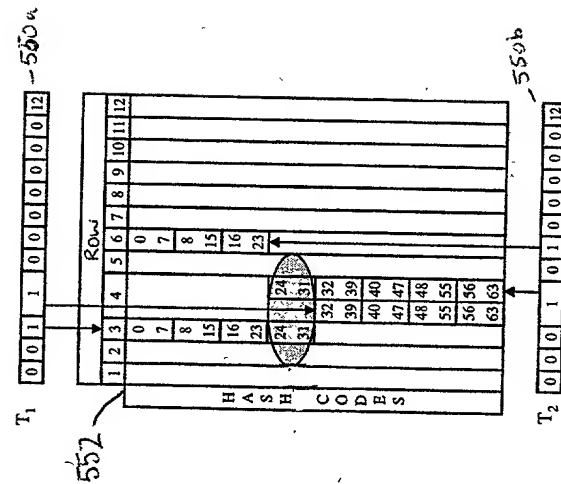


FIG. 10A

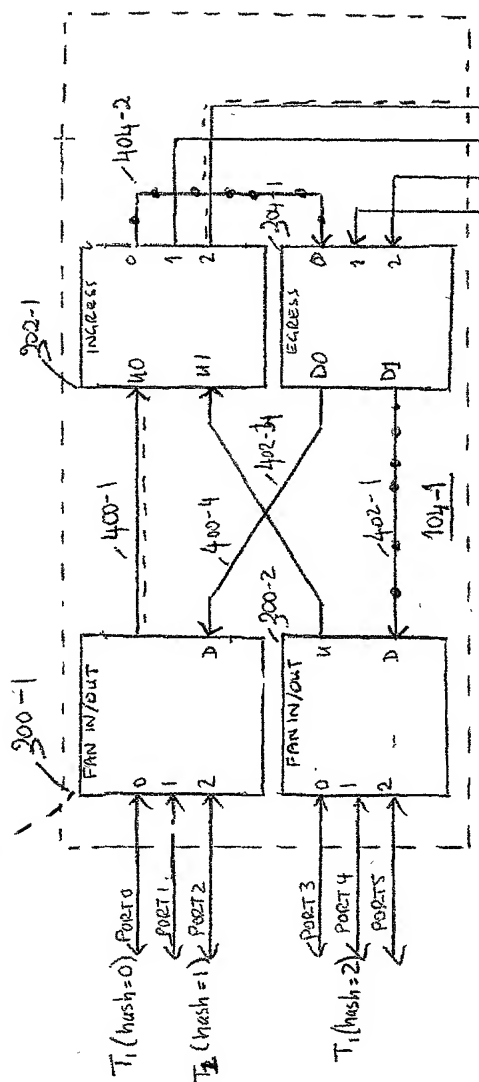
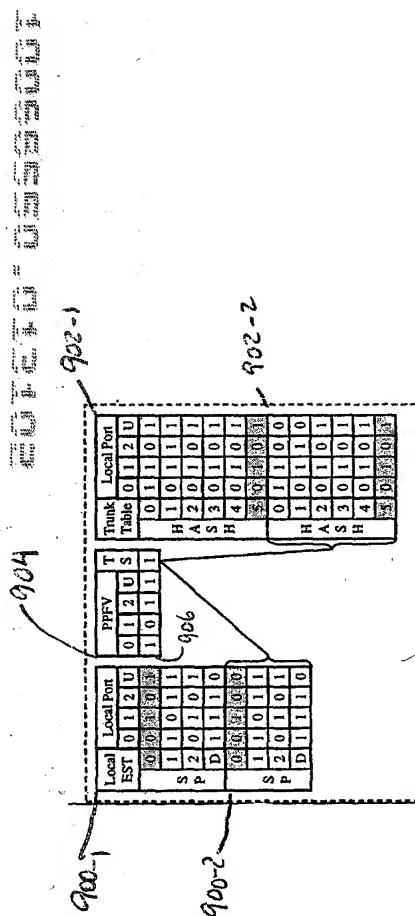


FIG. 11